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U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04) Application/Control Number: 09/866,800 Page 2

Art Unit: 2826

# **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Attorney J. Spong (Reg. No.: 52,241) on June 28, 2004.

The application has been amended as follows:

## **BEGIN EXAMINERS' AMENDMENT**

- (1) Claim 1, line 4: the wording "to pull down a pad to a ground level" has been replaced by "to pull down a pad";
- (2) Claim 1, line14: the wording "at a deeper position of" has been replaced by: "at a deeper position than":
- (3) Claim 1, line 15: the wording "near the second isolation region" has been replaced by: "adjacent to the second isolation region";
- (4) Claim 20, line 4: the wording to pull down a pad to a ground level" has been replaced by "to pull down a pad";
- (5) Claim 27, line 4: the wording "to pull down a pad to a ground level" has been replaced by "to pull down a pad".

### **END OF EXAMINER'S AMENDMENT**

#### **Drawings**

2. Replacement Sheets for drawings were received on 05/11/2004. The examiner accepts these drawings.

Application/Control Number: 09/866,800

Art Unit: 2826

## **REASONS FOR ALLOWANCE**

Claims 1, 2, 5, 6, 20, 21, 27 and 28 are allowed.

1. The following is an examiner's statement of reasons for allowance:

With regard to claims 1, 2, 5, 6, 27 and 28: Neither Natori nor Wolf '3-7 nor Wolf '5-3 teach or suggest a silicide layer connecting a "ground terminal connected to the second N-type diffusion region and a P-type diffusion region" (lines 21-22 of claim 1), while Chen et al (5,166,089) as cited previously (see PTO-892 of first Non-Final Action mailed 3/11/02) do not teach the first P-type diffusion region (on the side of the MOS transistor in relation to the isolation region closest to the latter, as opposed to on the opposite side as shown in Figure 2).

With regard to claims 20 and 21: Neither Natori nor Wolf '3-7 nor Wolf '5-3 teach or suggest a silicide layer connecting a "ground terminal connected to the second N-type diffusion region and a P-type diffusion region" (final two lines of claim 20), while Chen et al (5,166,089) as cited previously (see PTO-892 of first Non-Final Action) do not teach the first P-type diffusion region (on the side of the MOS transistor in relation to the isolation region closest to the latter, as opposed to on the opposite side as shown in Figure 2).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Johannes P Mondt whose telephone number is 571-272-1919. The examiner can normally be reached on 8:00 - 18:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on 571-272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JPM June 18, 2004